APPLICATION DATA SHEET

Inventor Information

Inventor One Given Name::

Khim Hong

Family Name::

NG

Name Suffix::

Postal Address Line One::

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Postal Address Line Two::

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State or Province::

Country::

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Inventor Two Given Name::

Yeow Keong

Family Name::

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Name Suffix::

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Block 725, Yishun Street 71, #01-03

Postal Address Line Two::

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Postal or Zip Code Citizenship Country::

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Inventor Three Given Name::

Kar Hwee

Family Name::

Name Suffix::

KOH

Postal Address Line One::

Block 4, Tanjong Pagar Plaza, #15-23

Postal Address Line Two::

City::

State or Province::

Country::

Singapore

Postal or Zip Code

081004

Citizenship Country::

Singapore

Correspondence Information

Correspondence Customer Number:: 6449

Application Information

Title Line One::

TITANIUM UNDERLAYER FOR LINES IN

SEMICONDUCTOR DEVICES

Title Line Two: **Total Drawing Sheets:**

5

Formal Drawings?::

Yes

Application Type::

U.S. National Phase Application

Docket Number::

3225-103

Secrecy Order in Parent Appl?::

No

Representative Information

Representative Customer Number:: 6449

Continuity Information

This application is a::

371

>Application One::

PCT/SG2003/000011

Filing Date::

January 20, 2003

Assignment Information

Assignee name::

SYSTEMS ON SILICON MANUFACTURING COMPANY

PTE. LTD.

Street of mailing address::

70 Pasir Ris Drive 1

City of mailing address::

State or Province of

mailing address::
Country of mailing address::

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Postal or Zip Code of

mailing address::

519527